IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional U.S. Patent Application of:

Jarrod ELIASON

Examiner:

Serial No.

Art Unit:

Filed: July 3, 2003

Title: FERROELECTRIC NON-VOLATILE

LOGIC ELEMENTS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.97 the Examiner may wish to consider the references listed on the attached Form PTO/SB/08A. In submitting these references for the Examiner's consideration, no representation is made or implied that the references are or are not material to the examination of the application. The Examiner is encouraged to make his or her own determination of materiality.

Each of these references were either cited or provided in U.S. Patent Application Serial No. 10/076,058, from which priority under 35 U.S.C. § 120 is claimed. Pursuant to 37 C.F.R. § 1.98, copies of the references are not provided.

No fee is believed due. However, any fees associated with this communication may be charged to Deposit Account No. 50-1123.

Respectfully submitted,

July 3, 2003

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PTO/SB/08A (10/01) (Substitute for form 1449A/PTO) INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) ATTY. DOCKET NO. RAM 493 DIV Client/Matter No. 80412.0139.003 FIRST NAMED INVENTOR: Jarrod ELIASON FILING DATE: July 3, 2003 ART UNIT:

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Sheet __1__ of __2___

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